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Hogan&Hartson, Tokyo 813-59084071

NO. 9507 P. 18/65

PATENT
81788.0163
RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2759

#14
Drawing
CVR
11.20.01
JC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Art Unit 2759

Yoshiyuki TANAKA, et al.

Examiner: D. Robertson

Serial No.: 09/130,818

Filed: August 7, 2001

For: METHOD FOR CONTROLLING
NON-VOLATILE SEMICONDUCTOR
MEMORY SYSTEM BY USING LOOK
UP TABLE (As Amended)

I hereby certify that this correspondence is
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14, 2001.

Sterlon R. Mason Reg. No. 41,179

Name

November 15, 2001

Signature

Date

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents
Washington, D.C. 20231
Attention: Box AF

Dear Sir:

Applicants respectfully request approval of one (1) sheet of proposed drawing changes with changes to Figure 5 as shown in red ink. Figure 5 has been amended to delete the text "(256 bytes)" found in the data block of physical block 0, page 0. No new matter has been added.

Respectfully submitted,

HOGAN & HARTSON, L.L.P

Date: November 15, 2001

By:

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0163.draw.doc

5/45

		0	511	512	527
PHYSICAL BLOCK 0	Page 0	DATA AREA (256BYTES)	REDUNDANT DIVISION (16BYTES)		
	Page 1				
	⋮				
	Page 15				
PHYSICAL BLOCK 1	Page 0				
	Page 1				
	⋮				
	Page 15				
⋮	⋮	⋮	⋮	⋮	⋮
PHYSICAL BLOCK 1023	Page 0				
	Page 1				
	⋮				
	Page 15				

FIG.5

PRIOR ART